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		PATENT NUMBER
Sub	Class	<p>BEST AVAILABLE COPY</p>
ISSUE CLASSIFICATION		

U.S. UTILITY Patent Application

<p>O.I.P.E.</p> <p>AS</p> <p>SCANNED SA ③ Q.A. A.G.</p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/827056	CONT/PRIOR D	CLASS 057 438	SUBCLASS 283	ART UNIT 2811	EXAMINER <i>[Signature]</i>
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Method of fabricating high-coupling ratio split gate flash memory
cell array

PTO-2040
12/89

ISSUING CLASSIFICATION											
ORIGINAL			CROSS REFERENCE(S)								
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
438		283	438	257	279	289	316				
INTERNATIONAL CLASSIFICATION											
A0	1	L	21	336							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 5	Figs. Drwg. 11	Print Fig. 24	Total Claims 15	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	Thomas Mager 10/9/03 _____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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